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To Examiner: Thai Q. Phan Technology Center 2100	Total Pages Sent: 13 (including cover sheet)
Facsimile Number: (703) 872-9306	Transmission Date: April 21, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Summerer Docket No.: 99 P 7719 US 02
Serial No: 09/733,665 Art Unit: 2128
Date Filed: December 8, 2000
TITLE: Semiconductor Structures and Manufacturing Methods

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- Fax Cover Sheet with Certificate of Transmission (1 page)
- Response (7 pages)
- Copy of Office Action from Serial No. 09/362,976 dated 09/12/2000 (5 pages)

Respectfully submitted,



Kristin Hayes
Legal Assistant

Confirmation Respectfully Requested

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Filed: December 8, 2000

Examiner: Phan, Thai Q.

For: Semiconductor Structures and Manufacturing Methods

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RESPONSE

Dear Examiner:

The following remarks are offered in response to the Examiner's Office Action dated February 2, 2004, and are respectfully submitted as a full and complete response to that Action.